

**Scholar Details:**

<b>External Scholars</b>							
<b>S.No</b>	<b>Candidate Name &amp; Designation</b>	<b>Presently working Institution/ Industry</b>	<b>Provisional Registration Details</b>		<b>Title of the Research Work</b>	<b>No. of Paper Published in Annexure- I</b>	<b>Present status of the PhD (Doing coursework/ Comprehensive viva completed/ Synopsis submitted/ Thesis submitted)</b>
			<b>Year of Reg.</b>	<b>Reg.No.</b>			
1.	Mr.J.Dhanansekar AP/ECE	Sri Eshwar College of Engineering, Coimbatore.	July 2016	16144697235	ASIC Implementation of RRAM by adiabatic Logic for low power applications.		Doing Course work
2.	Mr.D.Kumar AP/ECE	PA College of Engineering and Technology, Pollachi	July 2015	1514469853	Image Encryption with lossless / Lossy Compression		Comprehensive viva completed